Agenda: x86 Community Call May 2018

No new items were added to the agenda.

Project Management stuff to keep the Momentum going

[RFC XEN PATCH v4 00/41] Add vNVDIMM support to HVM domains

There are a few action's outstanding, but I believe that the original set of actions is maybe not appropriate anymore.

ACTION (March): Haozhong Zhang to drop the RFC and CC George and Roger
ACTION (March): Royger will help and give feedback. George will also be involved as he needs to review the memory side of the series. He will

ACTION (March): If needed - we can set up a meeting between Zhang and other stakeholders. Lars and John to take over an admin role to make sure developers can focus on the substance.

@George, @Royger: we should have a very brief update

[PATCH RFC 00/10] x86 passthrough code cleanup

ACTION (April - DONE): Brian Wood to ping Suravee Suthikulpanit (AMD)
@Wei: should this be closed?

ACTION (April - DONE): Lars to propose fixing CC issue in xen.git:MAINTAINERS copying the R section entries from Linux.git:
ACTION (April - DONE): Lars to have a back-channel discussion with AMD on how to solve this.
For now, CC Brian
Changes are committed in tree

PVHv2 Status (Royger)

The short-term resourcing issue around PVHv2 for Linux seems to be resolved.
@Roger: suggest to take this item off the agenda, bit will leave it in for now

PCI Emulation - Future Direction (Royger, Stefano, Julien)

The call took place: meeting minutes are at
https://lists.xenproject.org/archives/html/xen-devel/2018-05/threads.html#00262

@Royger, @Paul: Not sure whether you want to give a very brief update to the larger group

Recently unblocked dependencies / reviews

These may be worth covering in the call

AMD AVIC Series

This was review going normally: any change?

[RFC PATCH 0/8] Add guest CPU topology support

Sent in for meeting agenda by George
https://marc.info/?l=xen-devel&m=151538433419631
https://xen.markmail.org/thread/od46uc5nwhshnluz

Some feedback from Andrew Cooper and Daniel De Graaf

Dependencies: Andrew's CPUID work. Currently, this version doesn't have any dependency. But Andrew thought it was on the wrong direction. So Chao decided to wait for Andrew's work to finish and rework based on CPUID.

Note: Andrew mentioned that he is working on CPUID support. Any updates?

[PATCH RESEND v1 0/7] Intel Processor Trace virtulization enabling

v1.1 Posted by Kang, Luwei
https://xen.markmail.org/thread/rbaf7cxh2a7wwchf

Jan and Wei raised some questions last week and still under discussion.
Not all the patches have comments. So, it still need some time to reach an agreement, prepare patches and send new version.

Patches waiting for a new Revision

[RFC Patch v4 0/8] Extend resources to support more vcpus in single VM

From: Chao Gao
RFC v4 re-posted by Chao Gao: https://xen.markmail.org/thread/tlto7b3fadp7kkw6 (Dec 17)

Dependencies: Virtual interrupt remapping of virtual VT-d and Changes to IOREQ server is based on Paul Durrant's "x86: guest resource mapping".

[PATCH 0/7] paravirtual IOMMU interface

From: Paul Durrant
https://xen.markmail.org/thread/kmxk4hoj2ao65qsa
ACTION (March): Paul to resend the series with a clear problem statement.

[PATCH v4 0/4] x86/cpuid: enable new cpu features

From: Yang Zhong
Link: https://lists.xen.org/archives/html/xen-devel/2018-01/msg00049.html

Yang will pick up around mid-May

[PATCH v4 00/28] add vIOMMU support with irq remapping function of virtual VT-d

From: Chao Gao
v4 posted by Chao Gao: https://xen.markmail.org/thread/wfyorbn3nzsio6s7

Roger already gave some comments on the version 4.
Chao will follow up on a new version.

Blocked Series

[PATCH RFC 00/14] EPT-Based Sub-page Write Protection Support

RFC posted by Zhang Yi
https://xen.markmail.org/thread/m75h6b2aiwk5h7fx

No acks, reviews only by memaccess maintainers / developers
Issues: Use case for the feature is still not clear and needs discussion

[RFC PATCH v2 00/17] RFC: SGX Virtualization design and draft patches

From: Boqun Feng
Link: https://lists.xen.org/archives/html/xen-devel/2017-12/msg00104.html

We agreed that SGX has a server use-case. However the specifications for this feature are unclear and very large, which requires a reviewer to read the spec (takes 1-2 weeks) before he/she is able to review. In addition, the signing infrastructure for SGX is not yet in place, which makes this hard to understand.

ACTION (April): Lars to think about this, discuss with Intel (CC kai.huang@intel.com). Then get back to the group and discuss. Issue is about understanding how all the pieces fit together.